```
;------------------------ Boolean Equation Segment -----------------

EQUATIONS

;         TOGGLES                          RETAINS COUNT

F6 = ENF*(/F6*F0*F1*F2*F3*F4*F5   +F6*(/F0+/F1+/F2+/F3+/F4+/F5))
F5 = ENF*(/F5*F0*F1*F2*F3*F4      +F5*(/F0+/F1+/F2+/F3+/F4))
F4 = ENF*(/F4*F0*F1*F2*F3         +F4*(/F0+/F1+/F2+/F3))
F3 = ENF*(/F3*F0*F1*F2            +F3*(/F0+/F1+/F2))
F2 = ENF*(/F2*F0*F1               +F2*(/F0+/F1))
F1 = ENF*(/F1*F0                  +F1*(/F0))
F0 = ENF*/F0

FTC = /F0 * F1 * F2 * F3 * F4 * F5 * F6

ENF = (START_FB + ENF) * /FTC     ; starts on START_FB, ends on FTC

F0.TRST = /OE
F1.TRST = /OE
F2.TRST = /OE
F3.TRST = /OE
F4.TRST = /OE
F5.TRST = /OE
F6.TRST = /OE
FTC.TRST = /OE
ENF.TRST = /OE

WE_DAC = /F0

WE_DAC.TRST = /OE

;------------------------ Simulation Segment ----------------------

SIMULATION

TRACE_ON CLK128B ENF WE_DAC F0 F1 F2 F3 F4 F5 F6 FTC OE

PRELOAD /ENF

SETF /CLK128B /OE /START_FB

CLOCKF CLK128B

SETF START_FB

CLOCKF CLK128B

SETF /START_FB

FOR X := 1 TO 126 DO
BEGIN
CLOCKF CLK128B
END

FOR X := 1 TO 5 DO
BEGIN
CLOCKF CLK128B
END

SETF OE

TRACE_OFF
```